

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| S1 | 2 | "5841967".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:34 |
| S2 | 2 | "5109353".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 10:33 |
| S3 | 2 | "5036473".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 11:29 |
| S4 | 2 | ((synthesi\$5) same (accessibility with logic)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 11:34 |
| S5 | 22 | ((synthesi\$5) and (accessibility with logic)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 11:31 |
| S6 | 22 | ((synthesi\$5) same (accessib\$6 with logic)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 11:36 |
| S7 | 20 | S6 not S5 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 11:34 |
| S8 | 21 | ((synthesi\$5) same (interface adj bus)) and ((identif\$5) same (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 11:37 |
| S9 | 21 | ((synthesi\$5) same (interface adj bus\$2)) and ((identif\$5) same (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:25 |
| S10 | 42 | ((synthesi\$5) same (access adj port)) and ((locat\$4 or detect\$4 or identif\$5) same (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:43 |

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| S11 | 42 | S10 not S7 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:27 |
| S12 | 13 | ((synthesi\$5) with (access adj port)) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:45 |
| S13 | 22 | ((synthesi\$5) with ((access adj port) or (interface adj logic) or (accessibility adj logic))) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:50 |
| S14 | 2375 | ((synthesi\$5 or insert\$4 or creat\$4 or add\$4 or introduc\$4) with ((access\$4 with port) or (interface adj logic) or (accessibility adj logic))) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:52 |
| S15 | 700 | ((synthesi\$5 or insert\$4 or creat\$4 or add\$4 or introduc\$4) with ((access\$4 adj port) or (interface adj logic) or (accessibility adj logic))) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:53 |
| S16 | 2 | ((synthesi\$5 or insert\$4 or creat\$4 or add\$4 or introduc\$4) with ((access\$4 adj port) or (interface adj logic) or (accessibility adj logic))) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) and (duv or (design adj under adj verification)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:55 |
| S17 | 4 | ((synthesi\$5 or insert\$4 or creat\$4 or add\$4 or introduc\$4) with ((access\$4 adj port) or (interface adj logic) or (accessibility adj logic))) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) and ((design adj under adj test) or dut) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 14:12 |
| S18 | 700 | ((synthesi\$5 or insert\$4 or creat\$4 or add\$4 or introduc\$4) with ((access\$4 adj port) or (interface adj logic) or (accessibility adj logic))) and ((locat\$4 or detect\$4 or identif\$5) with (memories and registers)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:57 |

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| S19 | 29 | S18 and (vhdl or verilog) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 12:57 |
| S20 | 2140 | (IEEE with "1149.1") | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 14:12 |
| S21 | 59 | S20 and bsdl | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 16:13 |
| S22 | 2 | "6618839".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:38 |
| S23 | 2054 | 716/4.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 16:14 |
| S24 | 179 | 714/28.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 16:14 |
| S25 | 454 | 714/33.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 16:14 |
| S26 | 914 | 703/14.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 16:14 |
| S27 | 384 | 703/15.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/11 16:14 |
| S29 | 0 | "6618839".pn. and protocol | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:35 |

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| S30 | 0 | "6931572".pn. and protocol | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:35 |
| S31 | 0 | (design adj instrumentation adj circuitry) same protocol | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:36 |
| S32 | 0 | (design adj instrumentation adj circuitry) and protocol | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:36 |
| S33 | 2 | "6618839".pn. and DIC | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2006/01/13 09:39 |